

FIG. 1

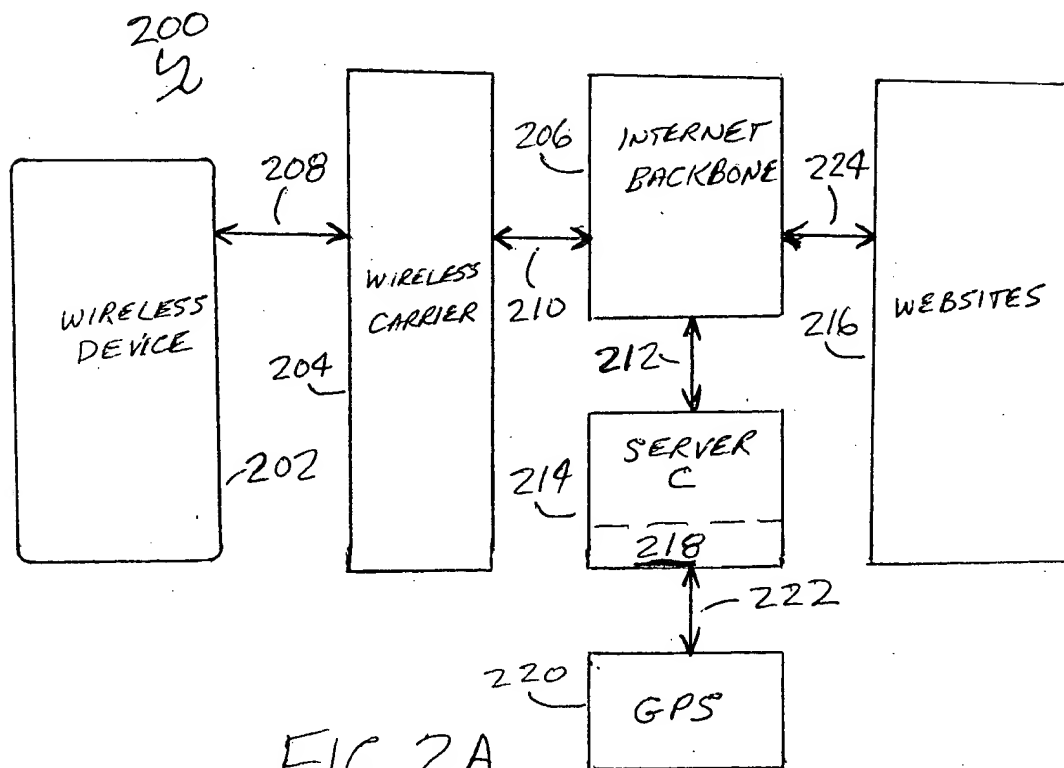
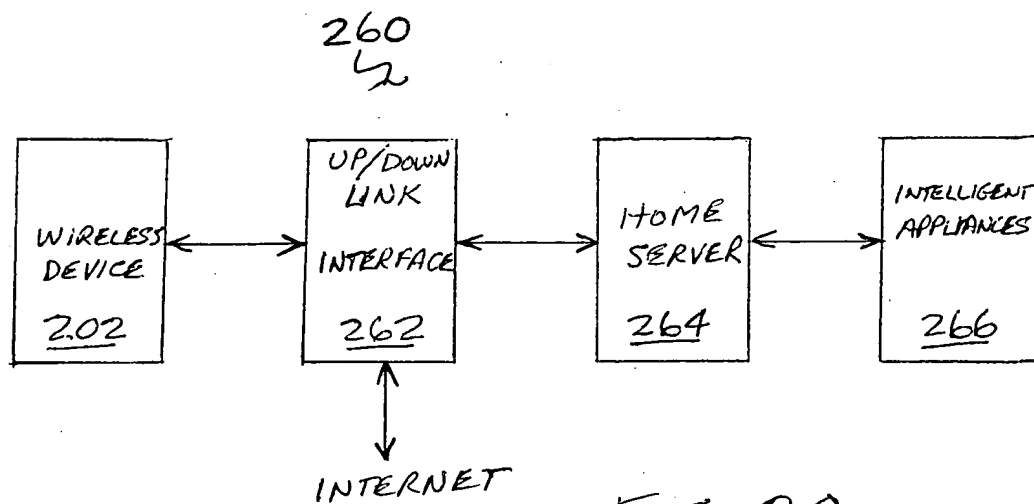
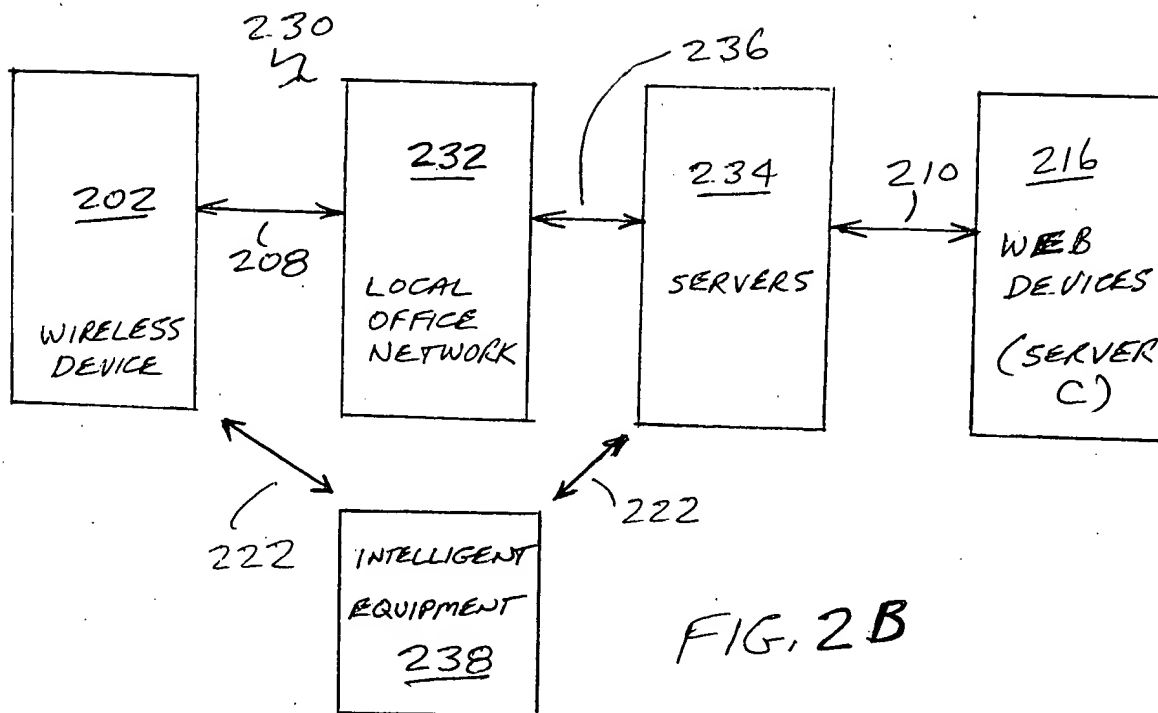


FIG. 2A

000000-000000



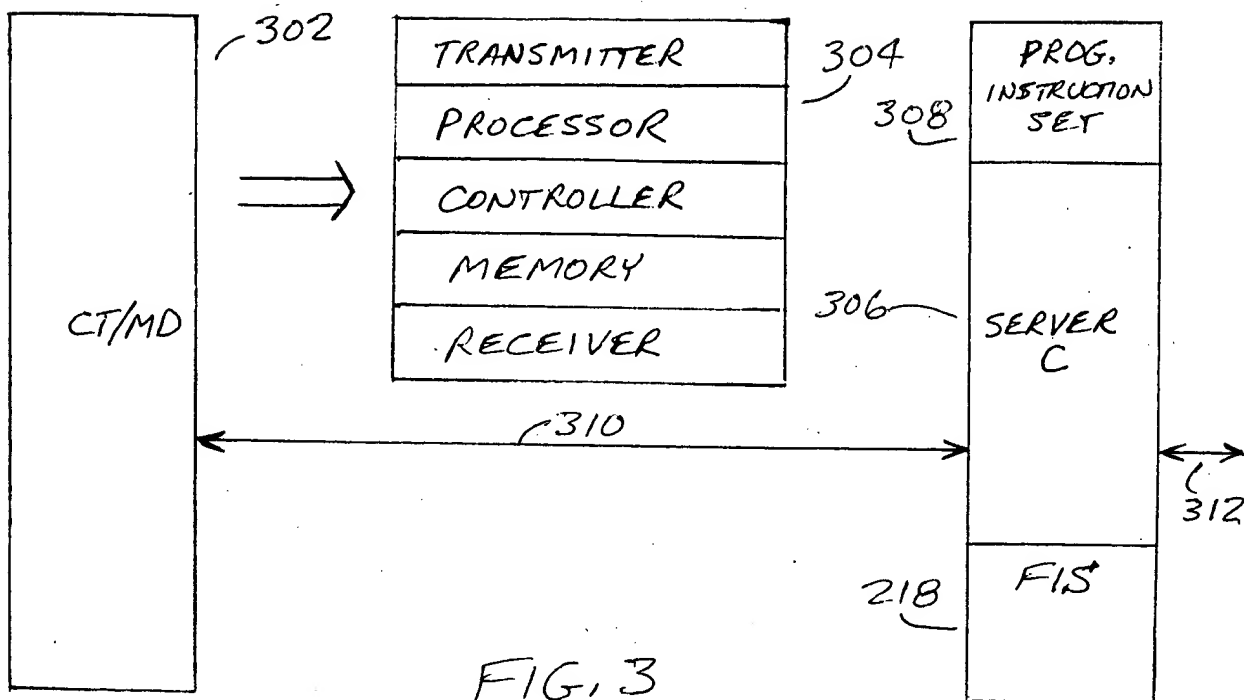


FIG. 3

400

CURRENT MODE - PRIORITY 402

PRIMARY	SECONDARY	TERTIARY
PUBLIC CARRIER LOOP	HOME LOOP	LOCAL OFFICE LOOP
FREQUENCY FP	FREQUENCY FH	FREQUENCY FL



REQUESTED MODE - PRIORITY 404

PRIMARY	SECONDARY	TERTIARY
LOCAL OFFICE LOOP	PUBLIC CARRIER	HOME LOOP
FL	FP	FH

FIG. 4

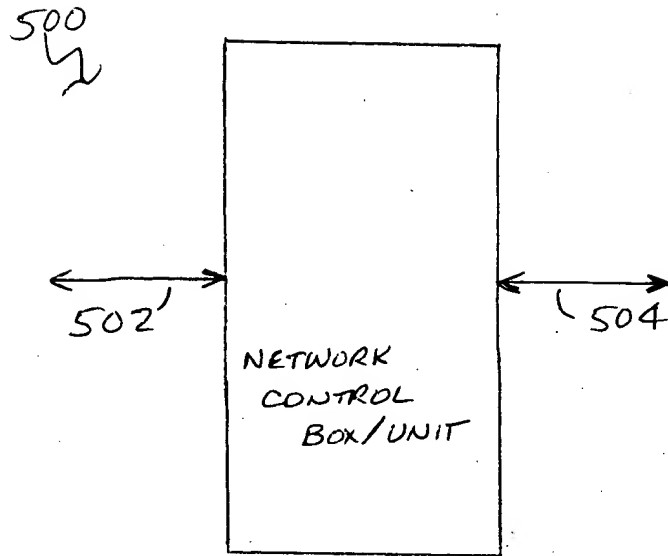


FIG. 5

600

TRANSMITTER	PROCESSOR	CONTROLLER	MEMORY	RECEIVER
<u>608</u>	<u>602</u>	<u>606</u>	<u>604</u>	<u>610</u>

FIG. 6

702

FREQUENCY SOURCE	PARAMETER
CHANNEL 1	f_1, P_1, BW_1 ---
CHANNEL 2	f_2, P_2, BW_2 ---
CHANNEL N	f_N, P_N, BW_N ---

704

FREQUENCY SOURCE	PARAMETER
CHANNEL 1	f_3, P_3, BW_3 ---
CHANNEL 2	f_4, P_4, BW_4 ---
CHANNEL N	f_M, P_M, BW_M ---

FIG. 7